

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,897,553 B2
APPLICATION NO. : 10/633925
DATED : May 24, 2005
INVENTOR(S) : Jerrold L. King and Jerry M. Brooks

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

In ITEM (75) "Inventors," after "Jerrold L. King" change "Boise, ID" to --Morgan Hill, CA--

In the drawings:

FIG. 5, delete reference numerals "24A", "24B",
"24C" and the corresponding lead lines
FIG. 7, change reference numeral "72" to --42--

In the specification:

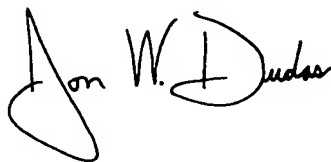
COLUMN 6, LINE 45, change "assemble" to --assembly--
COLUMN 6, LINE 48, change "none conductive" to --nonconductive--

In the claims:

CLAIM 1, COLUMN 8, LINE 6, change "section" to --sections--
CLAIM 14, COLUMN 8, LINE 62, change "flip chip" to --flip-chip--

Signed and Sealed this

Sixth Day of January, 2009



JON W. DUDAS
Director of the United States Patent and Trademark Office

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Replace FIG. 5 with the following:

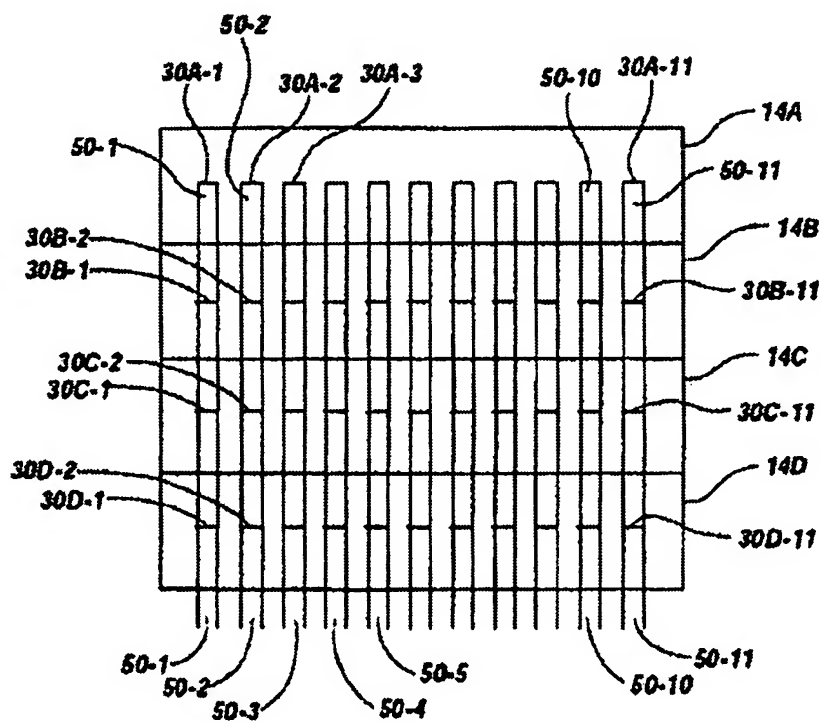


Fig. 5

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Replace FIG. 7 with the following:

